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ADAPT	TVE PREDISTORTIC	ling in connection with DN FOR A TRANSMIT of following documents:	application no. SYSTEM WITH	10/613,856, entitled GAIN, PHASE AND
•	REV/POA			
•	Statement Under 37	CFR 3.73(b)		
•	Copy of Assignment	of Patent Rights execute	ed 10/31/2005 (10	Pages)
•	Copy of Assignment	of Patent Rights execute	ed 12/23/2005 (16	pages)
•	CERTIFICATE	OF FACSIMILE TRA	ANSMISSION	
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PTO/SB/80 (04-05) Approved for use through 11/30/2005. OMB 0651-0035

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POWER OF ATTORNEY TO PROSECUTE APPLICATIONS BEFORE THE USPTO I hereby revoke all previous powers of attorney given in the application identified in the attached statement under 37 CFR 3.73(b) I hereby appoint: Practitioners associated with the Customer Number. 00043831 Practitioner(s) named below (if more than ten patent practitioners are to be named, then a customer number must be used): Registration Registration Number Number as attorney(s) or agent(s) to represent the undersigned before the United States Patent and Trademark Office (USPTO) in connection with any and all patent applications assigned only to the undersigned according to the USPTO assignment records or assignment documents attached to this form in accordance with 37 CFR 3.73(b). Please change the correspondence address for the application identified in the attached statement under 37 CFR 3.73(b) to: 00043831 The address associated with Customer Number, **OR** · Firm or Individua! Name Address City State Zio Country Telephone Assignee Name and Address: Zarbaña Digital Fund LLC 2711 Centerville Road, Suite 400 Wilmington, DE 19808 A copy of this form, together with a statement under 37 CFR 3.73(b) (Form PTO/SB/96 or equivalent) is required to be filed in each application in which this form is used. The statement under 37 CFR 3.73(b) may be completed by one of the practitioners appointed in this form if the appointed practitioner is authorized to act on behalf of the assignee, and must identify the application in which this Power of Attorney is to be filed. SIGNATURE of Assignee of Record The individual whose signature and title is supplied below is authorized to act on behalf of the assignee Signature Date Name Bryan Burpee, Authorized Person for Zarbaña Digital Fund LLC Telephone

This collection of Information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to title (and by the USPTO to process) an application. Confidentially is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gethering, prepring, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments of the expension of time you require to complete this form and/or suppositions for reducing this burden, should be sent to the Chief-Information-Officer. U.S. Patent and Tracemark Office, D.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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STATEMENT UNDER 37 CFR 3.73(b)
Applicant/Patent Owner: Zerbaña Digital Fund LLC
Application No./Patent No.: 10/613,856 6,975,167 Filed/Issue Date: 7/3/2003 12/13/2005
Entitled: ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS
Zarbaña Digital Fund LLC (Name of Assignee) a Limited Liability Company (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)
states that it is: 1. ☑ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest. The extent (by percentage) of its ownership interest is ————— % in the patent application/patent identified above by virtue of either:
A. [] An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel, Frame, or for which a copy thereof is attached.
OR
B. [/] A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:
Aryan Saed To: lcefyre Semiconductor Corporation The document was recorded in the United States Patent and Trademark Office at Reel 014254 Frame 0665 or or for which a copy thereof is attached.
2. From: IceFyre Semiconductor Corporation To: Icefyre Semiconductor, Inc.
The document was recorded in the United States Patent and Trademark Office at Reel, Frame, or for which a copy thereof is attached.
3. From: Icefyre Semiconductor, Inc. To: Zarbaña Digital Fund LLC
The document was recorded in the United States Patent and Trademark Office at Reel, Frame, or for which a copy thereof is attached.
[] Additional documents in the chain of title are listed on a supplemental sheet.
[√] Copies of assignments or other documents in the chain of title are attached. [NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.
1/23/06 Michelle Craig, Reg. No. 52,776
Date Typed or printed name
Telephone number Signature
Attorney at Law

This collection of Information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the Individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Pattern and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT ASSIGNMENT

IceFyre Semiconductor Corporation, c/o Fraser Milner Casgrain LLP, 99 Bank Street, Suite 1420, Ottawa, Ontario, K1P 1H4

(hereinafter "Assignor")

IceFyre Semiconductor, Inc., c/o Fraser Milner Casgrain LLP, 99 Bank Street, Suite 1420, Ottawa, Ontario, K1P 1H4

(hereinafter "Assignee")

WHEREAS the Assignor and Assignee executed the Intellectual Property Transfer Agreement dated October 3/,2005, which provides for the purchase by Assignee of certain patent rights of Assignor; and

WHEREAS the Assignor, in consideration of CAD one dollar (\$1.00) and other good and valuable consideration, the receipt of which is hereby acknowledged, do hereby sell, assign and transfer to the Assignee and its successors and assigns:

- Assignor's entire right, title and interest in Canada, the United States and throughout the world in and out to the patents and patent applications listed in Schedule A attached hereto, including any and all inventions described therein, and in any and all continuations-in-part, continuations, divisions, substitutes, reissues, re-examinations, or extensions thereof, and all other applications for patent relating thereto which have been filed, or hereafter shall be filed, in Canada, the United States or in any other jurisdiction and further including all rights under treaties to file and prosecute patent applications corresponding to the preceding patents and patent applications (the "Patents");
- (ii) all of Assignor's corresponding right, title and interest in and to any patents which may issue therefore, the same to be held and enjoyed by Assignee to the full end of the term for which the said patent is granted and maintained, as fully and entirely as the same could have been held and enjoyed by Assignor, and;
- the right to take action and recover in respect of any infringement of the Patents (iii) that took place prior to the date of this Assignment.

The Assignor hereby authorizes the issuance of any and all registrations for the Patents to the Assignee, its successors, assigns or legal representatives.

The Assignor hereby irrevocably designates and appoints the Assignee and its duly authorized officers and agents as the Assignor's agent and attorney in fact, to act for and on the Assignor's behalf and stead, to do all such lawful acts and things and to execute such further lawful assignments, documents, assurances, applications and other instruments as reasonably may be required by the Assignee, its successors, assigns or legal representatives, to obtain any and all

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registrations for the Patents and to vest the same in the Assignee, its successors, assigns or legal representatives.

The Assignor hereby agrees to execute and sign all documents required to effect a recordation of the assignment of the Patents and registrations thereof before the proper office or agency.

The remainder of this page is intentionally left blank.

EXECUTED at: To RONTO	this 3 day of October,, 2005
	IceFyre Semiconductor/Corporation
	(). A.
·	Name: / Jim Laird
	Title: / Director
	/

STATI	EMENT OF WITNESS
I, Scott Clark	, whose full post office address is , was
personally present and did see execute the above assignment.	who is known to me.
	Name: Name: State 2005

COUNTERPART SIGNATURE PAGE TO PATENT ASSIGNMENT

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

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Comments	penss		Request for Examination due December 3, 2005.			Issued	Abandoned		Abandoned		Abandoned, but still revivable through 1/06.			Abandoned, but still revivable through
Foreign Patents	Yes. See Below.						Yes. See Below.		;					
Fees Current and Paid	Yes		Yes	Yes	Yes	Yes	No		Yes	Yes	Yes	Yes	Yes	Yes
Published / Unpublished			Published	TBD	TBD	Published	Published		Published	Published	Published	Published	TBD	TBD
Number	6603352		2003-550250	7008505/2004	2824126.6	6,937,096	10/068,120		2455111	2818192.1	2748525.9	2003-518082	7001206/2004	20040269
Status	esued	Nationalized	Pending	Pending		panssi	Abandoned	Nationalized	Abandoned	Pending		Pending	Pending	Pending
Title	SWITCHED-MODE POWER AMPLIFIER INTEGRALLY PERFORMING POWER COMBINING	PCT Application	alization	Korean Nationalization	اےا	SWITCHED-MODE POWER AMPLIFIER INTEGRALLY PERFORMING POWER COMBINING (CIP)	SELECTABLE INVERSION/VARIABLE GAIN COMBINER FOR DIVERSITY RECEPTION IN RF	PCT Application	Canadian Nationalization	Chinese Nationalization	European Nationalization	Japanese Nationalization	Korean Nationalization	Norwegian Nationalization
Itom	CE-001	ICE-001PC		ICE-001KR	Γ	ICE-001CP	(CE-002	ICE-002PC	ICE-002CA	ICE-002CN	ICE-002EP	ICE-002JP	ICE-002KR	ICE-002NO

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IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

<u>Item</u>	Title	Status	Number	Published / Unpublished	Fees Current and Paid	Foreign Patents	Comments
ICE-003	PSUEDO-NOISE CARRIER SUPPRESSION/IMAGE I REJECTION UP AND DOWN CONVERTERS	Pending	10/094,826		Yes		Allowed. Issue Fee due about 1/20/03.
ICE-003PC	Ħ	Pending	PCT/CA02/01498	Published	Yes		Abandoned
ICE-004	R SEIVER	Pending	10/154,282	Published	Yes	PCT Application Pending.	
ICE-004PC	PCT Application	Pending	PCT/CA02/01497	Published	Yes		
ICE-005	Oscillator Frequency Offsets	Abandoned	10/155,107	Published	No	PCT Application Pending.	Abandoned
ICE-005PC	PCT Application	Pending	PCT/CA02/01499	Published	Yes		
ICE-006	PHASOR FRAGMENTATION CIRCUITRY AND METHOD FOR PROCESSING MODULATED SIGNALS HAVING NON-	Pending	10/273,908	Published	s }-	Yes. See Below.	
1CE-006PC	PCT Application	Nationalized					
ICE-006JP	nalization	Pending	T80	Not Published	Yes		
ICE-007	SYSTEMS AND MODULES FOR USE WITH TRELLIS-BASED DECODING	Pending	10/377,859	Published	Yes		
ICE-007PC	PCT Application	Abandoned	PCT/CA2004/0002 Published 82	Published	Yes		Abandoned
ICE-008	AL	Pending	10/629,644	Published	Yes	Yes, See Below.	
ICE-008PC	PCT Application Korean Nationalization	Nationalized	7001719/2005	Not Dublished	Yee		
ICE-008CN	1-1	Pending	TBD	Not Published	Yes		

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IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

		20,000	Manhor	Published /	Fees Current	Foreign Patents	Comments
III	11116	Status		Unpublished	and Paid		
ICE-008JP	Japanese Nationalization Pending	Pending	2004-525088	Not Published	Yes		
ICE-009	PARALLEL SCRAMBLER/DESCRA MBLER	Pending		Published	Yes	Yes. See Below.	
(CE-009PC	PCT Application	Pending	PCT/CA03/01132	Published	Yes		
ICE-010	COMPUTATIONAL CIRCUITS AND METHODS FOR PROCESSING MODULATED SIGNALS HAVING NON- CONSTANT ENVELOPES	Abandoned	09/918,106	Published	NO	Yes. See Below.	Abandoned
ICE-010PC	PCT Application	Nationalized					
ICE-010CA	nalization	Pending	2,455,277	тво	Yes		Abandoned
ICE-010CN	Chinese Nationalization	Pending	2818664.8	Published	Yes		
ICE-011PC	i I	Nationalized					
ICE-010EP	European Nationalization	Pending	2748528.3	Published	Yes		Response to Examination Report Due December 15, 2005.
ICE-010JP	Japanese Nationalization	Pending	2003-518144	Published	Yes		
ICE-010KR	Korean Nationalization	Pending	7001445/2004	TBD	Yes		
ICE-010NO	Norwegian Nationalization	Pending	20040367	TBD	≺es		Abandoned, but still revivable through 1/06.
ICE-010CP	COMPUTATIONAL CIRCUITS AND METHODS FOR PROCESSING MODULATED SIGNALS HAVING NON- CONSTANT ENVELOPES (CIP)	Pending	10/205,743	Published	Yes		

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

Item	<u> </u>	Status	Number	Published /	Fees Current and Pald	Foreign Patents	Comments
ICE-011	CHIREIX ARCHITECTURE USING LOW IMPEDANCE AMPLIFIERS	pans	6,836,183		Yes	Yes. See Below.	lssued
ICE-011JP	Japanese Nationalization Pending		TBD	ТВО	Yes		
ICE-011EP	European Nationalization Pending	Pending	ТВD	Published	Yes		Response to Examination Report Due December 17, 2005.
ICE-012	MEMORY SYSTEMS AND METHOD FOR USE IN TRELLIS- BASED DECODING	Pending	10/377,860	Published	Yes		
ICE-013	PREDISTORTION CIRCUIT FOR A TRANSMIT SYSTEM	Pending	10/613,355	Published	Yes		
ICE-013CP	PREDISTORTION CIRCUIT FOR A TRANSMIT SYSTEM (CIP)	Pending	10/641,370	Published	Yes		
ICE-014	A METHOD OF AND DEVICE FOR ANTENNAE DIVERSITY SWITCHING	Pending .	10/610,454	Published	Yes	PCT Application Pending.	
ICE-014PC	PCT Application	Pending	PCT/CA2004/0009 49	Published	Yes		Nationalization Due: 12/30/05.
ICE-015	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM	Allowed	10/613,372	Issue Fee Paid	Yes		Allowed and ready for Issuance. Issue Fee Paid
ICE-015CP	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM (CIP)	Allowable	10/641,372	Allowed	Yes		Allowed. Issue Fee Due 12/23/05.

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IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

ltem.	Title	Status	Number	Published / Unpublished	Fees Current and Paid	Foreign Patents	Comments
ICE-016	SWITCHED-MODE POWER AMPLIFIER USING LUMPED ELEMENT IMPEDANCE INVERTER FOR PARALLEL COMBINING	panssj	6,879,209	Published	Yes		Issued
ICE-016C1	SWITCHED-MODE POWER AMPLIFIER USING LUMPED ELEMENT IMPEDANCE INVERTER FOR PARALLEL COMBINING (CIP)	Pending	11/099,916	Published	Yes		Final (6 Month) date to respond to office action: December 17, 2005.
ICE-017	INTEGRATED CIRCUIT INCORPORATING WIRE BOND INDUCTANCE	Pending	10/610,497	Published	Yes		Notice of Appeal with Appeal Brief Due 12/30/05.
ICE-018	DIGITAL BRANCH CALIBRATOR FOR AN RF TRANSMITTER	Pending	10/627,881	Published	Yes		
ICE-019	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS	Allowed	10/613,856	Issue Fee Paid	Yes	PCT Application Pending.	Allowed and ready for Issuance. Issue fee paid. Checking status.
ICE-019PC	PCT Application	Pending	CA/2004/000972	Published	Yes		
ICE-019CP1	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS (CIP)	Allowable	10/641,371	Allowed	Yes		Allowed. Issue fee due November 22, 2005.

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

ltem		Status	Number	Published / Unpublished	Fees Current and Paid	Foreign Patents	Comments
ICE-019CP2	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS (CIP)	Allowable	10/641,374	Allowed	Yes		Allowed. Issue fee due December 6, 2005,
ICE-019CP3	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS (CIP)	Allowable	10/641,373	Allowed	Yes		Allowed. Issue fee due November 24, 2005.
ICE-020	STAGGERED AGC WITH DIGITALLY CONTROLLED VGA	Pending	10/661,945	Published	Yes	PCT Application Filed.	
ICE-020PC	PCT Application	Pending	CA2004/001566	Published	Yes		
ICE-021	OPTIMIZED FFT/IFFT MODULE	Pending	10/662,063	Published	Yes		
ICE-022	METHOD FOR AMPLITUDE INSENSITIVE PACKET DETECTION	Pending	10/661,943	Published	Yes	PCT Application Filed.	
ICE-022PC	PCT Application	Pending	CA2004/001565	Published	Yes		
ICE-023	FREQUENCY DOMAIN EQUALIZER FOR WIRELESS COMMUNICATIONS SYSTEM	Pending	10/661,147	Published	Yes	PCT Application Filed.	
ICE-023PC	PCT Application	Pending	CA2004/001564	Published	Yes		
ICE-029	METHODS AND SYSTEMS FOR SIGNAL AMPLIFICATION THROUGH ENVELOPE REMOVAL AND RESTORATION	Pending	10/779,322	Not Published	Yes	PCT Application Filed.	
ICE-029PC	PCT Application	Pending	CA2005/000153	Not Published	Yes		

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

•	\(\frac{1}{2}\)	01-1-10	Number		Fees Current	Foreign Detente	on monte
iiiaiii	ille	Status	Mailibei	Unpublished	and Paid		201111120
ICE-030	SYSTEMS AND METHODS FOR RAPID SIGNAL DETECTION AND IDENTIFICATION	Pending	10/883,170	Not Published	Yes		
ICE-031	Multiple Input, Multiple Output Communications Systems	Pending	10/684,633	Not Published	Yes	-	
ICE-031C1	Multiple Input, Multiple Output Communications Systems (Continuation)	Pending	10/954,429	Not Published	Yes		
ICE-032	Power Amplifier	Pending	10/884,627	Not Published	Yes		
ICE-033PR	Improved Power Amplifier and Related Methods.	Pending	60/625,301	Not Published	∖es		Provisional application. Conversion due November 5, 2005.

Exhibit B

ASSIGNMENT OF PATENT RIGHTS

patents or patent applications to which any of the foregoing claim priority, and (c) current or future rights to (i) provisional patent any provisional patent application, patent application or patent listed below and all other rights arising out of such inventions and Delaware limited liability company, having an office at 2711 Centerville Road, Suite 400, Wilmington, New Castle Country, DE categories (a), (b), (c) and (d), including, without limitation, under the Paris Convention for the Protection of Industrial Property, protection, design patent protection, and other governmental grants; (d) the rights to all inventions and discoveries described in discoveries; (e) rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial whether currently pending, filed, or otherwise) and other enforcement rights, including, without limitation, all rights under the applications, patent applications, and patents of any kind relating to any inventions and discoveries described in any provisional provisional patent applications, patent applications and patents listed below and/or under or on account of any of the foregoing he International Patent Cooperation Treaty, or any other convention, treaty, agreement or understanding; (f) causes of action 19808 ("Assignee"), or its designees, all right, title and interest that exist today and may exist in the future in and to all of the For good and valuable consideration, the receipt of which is hereby acknowledged, IceFyre Semiconductor, Inc., counterparts to any of the foregoing, including, without limitation, certificates of invention, utility models, industrial design following (the "Patent Rights"): (a) the provisional patent applications, patent applications and patents listed below, (b) all continuations in part, continuing prosecution applications, and divisions of such patents and applications; and (iii) foreign design protections, design patent protections or other governmental grants of any type related to the any of the foregoing Delaware Corporation, ("Assignor"), does hereby sell, assign, transfer and convey unto Zarbaña Digital Fund LLC, a patent applications, patent applications and patents listed below; (ii) reissues, reexaminations, extensions, continuations, categories (b), (c) and/or (d) to

- damages,
- injunctive relief and
- iii) other remedies of any kind

for past, current and future infringement; and

(g) all rights to collect royalties and other payments under or on account of any of the foregoing.

<u>Item</u>	Title	Status	Number	Country	Inventor	Filing Date
ICE-001	Switched-Mode Power Amplifier Integrally Performing Power Combining	Issued	6,603,352	U.S.A.	Wight, James	12/3/2001
ICE-001PC	Switched-Mode Power Amplifier Integrally Performing Power Combining	Nationalized	CA02/01847	PCT	Wight, James	12/3/2002
ICE-001JP	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2003-550250	Japan	Wight, James	12/3/2002
ICE-001KR	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	7008505/2004	Korea	Wight, James	06/03/2004
ICE-001CN	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2824126.6	China	Wight, James	12/3/2001
ICE-001AU**	Switched-Mode Power Amplifier Integrally Performing Power Combining	Lapsed	2002351903	Australia	Wight, James	12/3/2002

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<u>Item</u>	Title	Status	Number	Country	Inventor	Filing Date
ICE-001CP	Switched-Mode Power Amplifier Integrally Performing Power Combining (CIP)	Issued	6,937,096	U.S.A.	Wight, James	6/30/2003
IĊE-002PR	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Expired	60/307/889	U.S.A.	Wight, James	10/1/2/1
ICE-002	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned	10/068,120	U.S.A.	Wight, James	2/6/2002
ICE-002PC	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Nationalized	CA02/01150	PCT	Wight, James	7/26/2002
ICE-002CA	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Abandoned	2455111	Canada	Wight, James	7/26/2002
ICE-002CN	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Pending	2818192.1	China	Wight, James	7/26/2002
ICE-002EP	Reception Diversity Combiner	Pending	2748525.9	ЕРО	Wight, James	7/26/2002

10/094,826 U.S.A.		n Allowed 10/094,826
10/094,826 CA02/01498 2002328744 10/154,282		Allowed Expired Lapsed Pending
	Allowed Expired Lapsed Pending	

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-004PC	Up/Down Conversion Circuitry for Radio Transceiver	Expired	CA02/01497	PCT	Birkett, Alexander	10/4/2002
ICE-004AU**	Up/Down Conversion Circuitry for Radio Transceiver	Lapsed	2002328743	Australia	Birkett, Alexander	10/4/2002
1CE-005	Oscillator Frequency Offsets	Abandoned	10/155,107	U.S.A.	Birkett, Alexander	5/23/2002
ICE-005PC	Frequency Offset Generator for Synthesized Signals	Expired	CA02/01499	PCT	Birkett, Alexander	10/4/02
ICE-005AU**	Frequency Offset Generator for Synthesized Signals	Lapsed	2002328745	Australia	Birkett, Alexander	10/4/2002
ICE-006	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	10/273,908	U.S.A.	Parker, Kevin	10/18/2002
ICE-006JP	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	2004-543858	Japan	Parker, Kevin	04/15/2005

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Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-006AU**	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Lapsed	2003278003	Australia	Parker, Kevin	10/14/2003
ICE-006PC	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Expired	2004036862	PCT	Parker, Kevin	4/29/2004
ICE-007	Systems and Modules for Use with Trellis-Based Decoding	Pending	10/377,859	U.S.A.	Amer, Maher	2/28/2003
ICE-007PC	Viterbi Decoder Operating In Units Of a Plurality Of Transitions	Expired	CA04/000282	PCT	Amer, Maher	2/26/04
ICE-008PR	Parallel Convolutional Encoder	Expired	60/399,728	U.S.A.	Amer, Maher	8/1/2002
ICE-008	Parallel Convolutional Encoder	Pending	10/629,644	U.S.A.	Amer, Maher	7/29/2003
ICE-008KR	Parallel Convolutional Encoder	Pending	7001719/2005	Korea	Amer, Maher	01/31/2005
ICE-008CN	Parallel Convolutional Encoder	Pending	03818236.X	China	Amer, Maher	07/31/2003
ICE-008JP	Parallel Convolutional Encoder	Pending	2004-525088	Japan	Amer, Maher	03/24/2005

Date	1/03	2003	/02	2003	2003	2003
Filing Date	07/31/03	7/31/2003	9/18/02	7/29/2003	7/31/2003	7/31/2003
Inventor	Amer, Maher					
Country	PCT	Australia	U.S.A.	U.S.A.	PCT	Australia
Number	CA03/0113	2003249822	60/411,343	10/629,640	CA03/01132	2003249821
Status	Nationalized	Lapsed	Expired	Pending	Expired	Lapsed
Title	Parallel Convolutional Encoder	Parallel Convolutional Encoder	Parallel Scrambler Descrambler	Parallel Scrambler/Descrambler	Parallel Scrambler/Descrambler	Parallel Scrambler/Descrambler
<u>Item</u>	ICE-008PC	ICE-008AU**	ICE-009PR	ICE-009	ICE-009PC	ICE-009AU**

<u>Item</u>	Title	Status	Number	Country	Inventor	Filing Date
ICE-010	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned	09/918,106	U.S.A.	Wight, James	7/30/2001
ICE-010PC	Signal Decomposition for The Control Of its Dynamic Range	Nationalized	CA02/001174	PCT	Wight, James	7/29/2002
ICE-010CA	Signal Decomposition for The Control Of its Dynamic Range	Abandoned but Revivable	2,455,277	Canada	Wight, James	7/29/2002
ICE-010CN	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Pending	20818664.8	China	Wight, James	7/29/2002
ICE-010EP	Signal Decomposition for The Control Of its Dynamic Range	Pending	2748528.3	EPO	Wight, James	7/29/2002
ICE-010JP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned	2003-518144	Japan	Wight, James	7/29/2002
ICE-010KR	Computational Circuits and Methods for Processing Modulated Signals Having	Pending	7001445/2004	Korea	Wight, James	01/30/2004

Item	Title	Status	Number	Country	Inventor	Filing Date
	Non-Constant Envelopes					
ICE-010NO	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned but Revivable	20040367	Norway	Wight, James	1/27/2004
ICE-010CP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes (CIP)	Pending	10/205,743	U.S.A.	Wight, James	7/26/2002
ICE-011	Chireix Architecture Using Low Impedance Amplifiers	Issued	6836183	U.S.A.	Wight, James	10/16/2002
ICE-011JP	Chireix Architecture Using Low Impedance Amplifiers	Pending	2004-543859	Japan	Wight, James	04/15/2005
ICE-011PC	Chireix Architecture Using Low Impedance Amplifiers	Nationalized	CA03/001546	PCT	Wight, James	10/14/2003
ICE-011EP	Chireix Architecture Using Low Impedance Amplifiers	Pending	03769084	EPO	Wight, James	10/14/2003

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-011AU**	Chireix Architecture Using Low Impedance Amplifiers	Trapsed	2003278004	Australia	Wight, James	10/14/2003
ICE-012	Memory Systems and Method for Use In Trellis-Based Decoding	Pending	10/377,860	U.S.A.	Amer, Maher	2/28/2003
ICE-013	Predistortion Circuit for a Transmit System	Pending	10/613,355	U.S.A.	Saed, Aryan	7/3/2003
ICE-013CP	Predistortion Circuit for a Transmit System (CIP)	Pending	10/641,370	U.S.A.	Saed, Aryan	8/13/2003
ICE-014	A Method Of and Device for Antennae Diversity Switching	Pending	10/610,454	U.S.A.	Saed, Aryan	6/30/2003
ICE-014PC	A Method Of and Device for Receive Antennae Diversity Switching	Pending	CA04/000949	PCT	Saed, Aryan	6/23/04
ICE-015	Adaptive Predistortion for a Transmit System	Allowed	10/613,372	U.S.A.	Saed, Aryan	7/3/2003
ICE-015CP	Adaptive Predistortion for a Transmit System (CIP)	Allowed	10/641,372	U.S.A.	Saed, Aryan	8/13/2003

Filing Date	7/8/2003	4/6/2005	6/30/2003	7/25/2003	7/3/2003	6/30/2004
Inventor	Grundingh, Johan	Grundingh, Johan	Wight, James	Saed, Aryan	Saed, Aryan	Saed, Aryan
Country	U.S.A.	U.S.A.	U.S.A.	U.S.A.	U.S.A.	PCT
Number	6,879,209	11/099,916	10/610,497	10/627,881	10/613,856	CA04/000972
Status	Issued	Pending	Pending	Pending	Allowed	Pending
Title	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining (CIP)	Integrated Circuit Incorporating Wire Bond Inductance	Digital Branch Calibrator for An RF Transmitter	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments	Adaptive Predistortion for a Transmit System with Gain,
Item	ICE-016	1CE-016CP	ICE-017	ICE-018	ICE-019	ICE-019PC

Item	Title	Status	Number	Country	Inventor	Filing Date
	Phase and Delay Adjustments	:				
ICE-019CP1	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,371	U.S.A.	Saed, Aryan	8/13/2003
ICE-019CP2	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,374	U.S.A.	Saed, Aryan	8/13/2003
ICE-019CP3	Adaptive Predistortion for a Transmit System with Guin, Phase and Delay Adjustments (CIP)	Allowed	10/641,373	U.S.A.	Saed, Aryan	8/13/2003
ICE-020	Staggered AGC with Digitally Controlled VGA	Pending	10/661,945	U.S.A.	Birkett, Neil	9/12/2003
ICE-020PC	Staggered AGC with Digitally Controlled VGA	Pending	CA04/001566	PCT	Birkett, Neil	8/26/2004
ICE-021	Optimized FFT/IFFT Module	Pending	10/662,063	U.S.A.	Amer, Maher	9/12/2003
ICE-022	Method for Amplitude Insensitive Packet Detection	Pending	10/661,943	U.S.A.	Birkett, Neil	9/12/2003

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Filing Date	8/26/2004	9/12/2003	8/26/04	2/13/2004	2/7/2005	07/01/2004	07/02/2004
Inventor	Birkett, Neil	Saed, Aryan	Saed, Aryan	Wight, James	Wight, James	Moher, Michael L.	Wight, James
Country	PCT	U.S.A.	PCT	U.S.A.	PCT	U.S.A.	U.S.A.
Number	CA04/001565	10/661,147	CA04/001564	10/779,322	CA05/000153	10/883,170	10/884,633
Stutus	Pending	Pending	Pending	Pending	Pending	Pending	Pending
Title	Method for Amplitude Insensitive Packet Detection	Frequency Domain Equalizer for Wireless Communications System	Frequency Domain Equalizer for Wireless Communications System	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Systems and Methods for Rapid Signal Detection and Identification	Multiple Input, Multiple Output Communications Systems
Item	ICE-022PC	ICE-023	ICE-023PC	ICE-029	ICE-029PC	ICE-030	ICE-031

Filing Date	09/30/2004	7/02/2004	11/05/2004	11/07/2005
Inventor	Wight, James	Parker, Kevin	Grundlingh, Johan	Grundlingh, Johan
Country	U.S.A.	U.S.A.	U.S.A.	U.S.A.
Number	10/954,429	10/884,627	60/325,301	Not yet assigned
Status	Pending	Pending	Expired	Pending
Title	Multiple Input, Multiple Output Communications Systems (CIP)	Power Amplifier	Improved Power Amplifier and Related Methods.	Power Amplifier
Item	ICE-031C1	ICE-032	ICE-033PR	ICE-033

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assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives. The terms and conditions of this Assignment of Patent Rights shall inure to the benefit of Assignee, its successors,

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at

ASSIGNOR

By: Muley A30

Title: Director

Nume:

(Signature MUST be notarized)

STATE OF MASSOCHUSERS

COUNTY OF WILLSELL)

same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the Notary Public in and for said State, personally known to me (or proved to me on the basis of satisfactory before me, the person acted, executed the instrument. personally appeared 煝

WITNESS my hand and official scal.

Signature Milital

